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 From: Jacqueline J. Garner
 Texas Instruments Incorporated
 Phone: 214-532-9348

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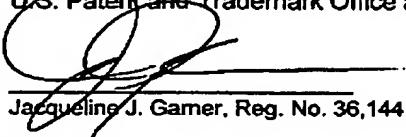
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Chambers	Art Unit:	2829
Serial No.:	10/649,425	Examiner:	Sarkar, A.
Filing Date:	08/27/2003	Docket No.:	TI-35714
Customer No.:	23494	Conf. No.:	5169

Title: STRUCTURE AND METHOD TO FABRICATE SELF-ALIGNED TRANSISTORS
 WITH DUAL WORK FUNCTION METAL GATE ELECTRODES

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NAME OF INVENTOR(S): Chambers	
TITLE OF INVENTION: STRUCTURE AND METHOD ...	
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Texas Instruments Incorporated
 PO Box 655474, M/S 3999
 Dallas, TX 75074